

# Simberian News: December 4, 2024

**Subject: Exciting New Features in Simbeor 2024.02!**

We are thrilled to announce the latest updates in Simbeor 2024.02, designed to enhance your signal integrity analysis experience and extend Simbeor's accuracy to mm-wave bandwidth. Here are the new features ([the complete list with instructions is available here](#)):

## SI Compliance Analyzer (SI CA):

- New de-embedded component port models to improve accuracy at high frequencies (over 30 Gbps data rates).
- IO to Pin models to account for connector and pin models in compliance analysis.
- Ability to define compliance conditions with equations.
- Compliance conditions for TDR measurements.
- Improved stitching via search algorithm for staggered vias.
- Signal direction identification and redefinition capabilities.
- New VTF and VTF\_XT metrics for the UCle standard.
- Goodness measure added for Pass condition in the Compliance Browser.
- Python/AdvGeoKit with examples of JSON descriptions of complex vias and tabbed routing design in Simbeor SDK.
- Results and compliance report generator from Lua script.

## Via Analyzer (VA):

- De-embedded coaxial and via-ports to improve accuracy of transitions to BGA and connectors at high frequencies (over 30 Gbps data rates).

## Linear Network:

- Ability to define IO to Pin models in External IO properties.
- New parameterized PKG element as defined in COM specs, which can be used to identify reference package models.

De-embedded vertical ports, supported in both SI CA and VA, improve the accuracy of transitions to BGA balls, coaxial and other connectors. They also facilitate the design of viaholes using the vertical segmentation approach – more on this in [the upcoming DesignCon 2025 paper](#) – don't miss it!

We hope you find these new features useful in your projects. Thank you for your continued support!  
Happy December!

**Team Simberian**

Sales Email:	<a href="mailto:sales@simberian.com">sales@simberian.com</a>	Simberian Inc.
Support Email:	<a href="mailto:support@simberian.com">support@simberian.com</a>	615 Hampton Dr. Unit B306.
Web Site:	<a href="http://www.simberian.com">www.simberian.com</a>	Venice, CA 90291
Telephone:	1-702-876-2882	USA

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